

	Monday	Tuesday	Wednesday	Thursday	Friday
	Day 1	Day 2	Day 3	Day 4	Day 5
09:00 - 09:30	Welcome by Host	Passive components (Rui TU/e)	Optimize your design with circuit simulation - passive photonic ICs (Andre VPI)	Photonic Building Blocks: how fabrication and physics can drive design automation Designing a photonics IC chip: using PDK, layout and verification (Bowen Phoenix)	Using the HHI/FhG PDK Tx/Rx platform: layout and DRC (Bowen, Phoenix)
09:30 - 10:30	JePPIX: Photonic IC design and manufacturing, our role in the ecosystem (Rui as JePPIX)				
10:30 - 11:00	coffee break	coffee break	coffee break	coffee break	coffee break
11:00 - 12:30	HHI foundry overview	AWG and MMIs SOA: structures, basic properties, amplification and thermal issues (Rui TU/e)	Optimize your design with circuit simulation - lasers and other active photonic devices (Andre VPI)	Chip characterizations and measurements Process Tolerances and Device Designs (Rui TU/e)	Verification and Design Rules Checking in Photonic IC design (Bowen, Phoenix)
	SMARTPhotonics foundry overview				
12:30 - 13:30		lunch	lunch	lunch	lunch
13:30 - 15:00	InP technologies: manufacturing (Victor HHI)	Photon Design	Optimize your design with circuit simulation - photonic ICs combining active and passive functions (Andre VPI)	Process Tolerance Analysis with Photonic IC circuit simulator (Bowen Phoenix)	Design for test and packaging: using die and package templates from HHI/FhG and Linkra (Bowen, Phoenix)
				Using the TriPlex™ PDK: layout and DRC (Bowen Phoenix)	
15:00 - 15:30	coffee break	coffee break	coffee break	coffee break	coffee break
15:30 - 17:00	Overview JePPIX software DEMO: Design for Packaging. What happens with our design when we try to package it as a product	Photon Design	Optimize your design with circuit simulation - integrate chip layout and PDK information (Andre VPI)	Using the SmartPhotonics PDK: layout and DRC (Bowen, Phoenix)	Design experience by VLC Photonics, BRIGHT Photonics
	Software installation				
17:00 - 17:30		Discussion	Discussion	Discussion	Discussion and evaluation
19:00 - 21:00		Workshop dinner			

TU/e

HHI

SMARTPhotonics

VPI

Phoenix

Photon Design

Filarete (by Phoenix)

Phoenix + VPI+ Photon Design