



Joint European Platform for InP-based Photonic Integrated Components and Circuits

Announcing the 5th JePPIX training in InP Photonic Integrated Circuit Design

25th of October – 5th of November

**Technische Universiteit Eindhoven
The Netherlands**

- Rare opportunity to study InP based PIC design and practical fab aspects
- Hands-on experience with various software tools
- Get your designs built through our shared project runs by the COBRA facility at TU/e.

More details are to be found on the JePPIX website.

The fee (excluding travel and hotel costs) will be € 1000,- .
There is a 50% University discount.

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